

Official launch: jan. 2026

AXEMIC3 CHIP



Unique functions for demanding applications

KEY FEATURES



MUTE: Radio silence mode, undetectable



File System: Secure access hierarchy



Memory: High capacity

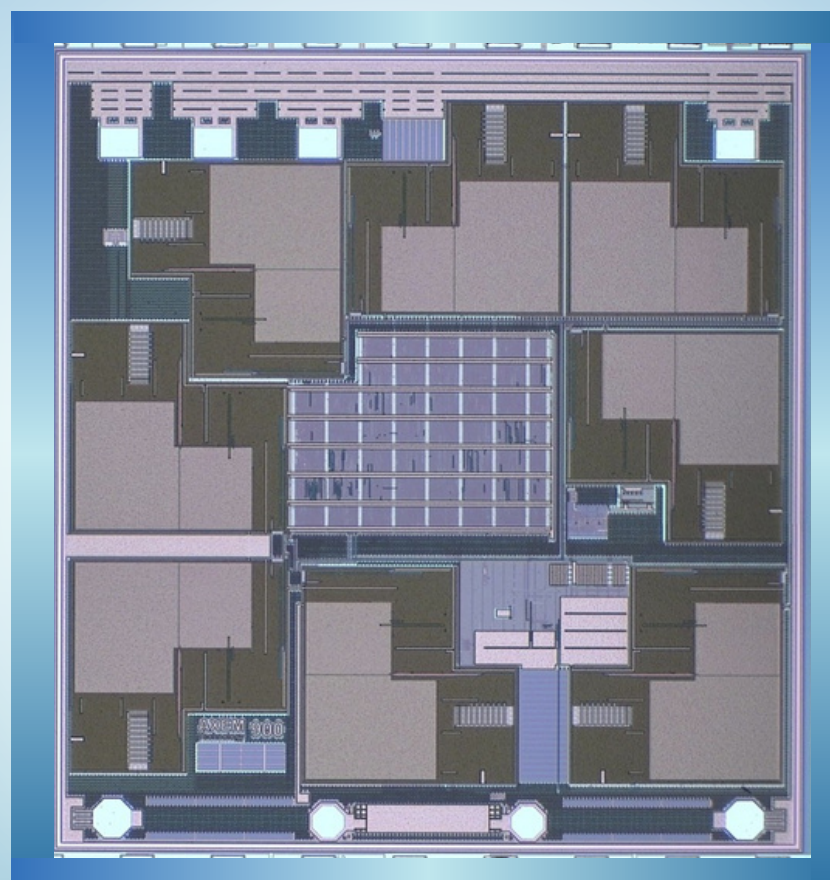
- User memory: 62 Kbit
- XTID: 96 bit
- EPC: 496 bit



I2C communication:
Remote power supply and sensor control



Data logger : Data accessible when the device is off



ATA Spec 2000, Dual & Multi record compatible
EPC UHF Global Class 1 Gen 2 / ISO 18000-6C
Large band 860-930 MHz

Commercial@axemtec.com

